

The Department of Electrical and Computer Engineering (ECE) at the George Washington University (GWU) invites applications for two tenure track faculty appointments at the Assistant or Associate Professor levels. The ECE Department anticipates growth and investment in the following areas:

- 1) Computer architecture including but not limited to parallel and multicore architectures, interconnects, network on chips (NoCs), architectures for embedded and reconfigurable systems, heterogeneous architectures, dependable and secure architectures including fault-tolerant and resilient architectures, power/energy-efficient computing architectures.
- 2) Next generation VLSI/CAD design including design automation, testing, low-power circuits and devices, system-on-chips, new and emerging design technologies, and chip integration with novel devices and materials as found in nanoscale electronics and/or nanophotonics.

Basic Qualifications: Applicants must have an earned doctorate in Electrical and Computer Engineering or a related discipline. A demonstrated commitment to excellence in teaching at both undergraduate and graduate levels is expected. It is also essential that the applicants demonstrate a solid publication record and have established a strong, externally sponsored research program, or have potential to do so, and be willing to engage in existing and future large collaborative research endeavors.

More information about the department is available at <http://www.ece.seas.gwu.edu/>. To apply, please choose the most suitable link below for your desired position and complete the online application. Please upload a cover letter indicating your primary area(s) of expertise and interest and desired professorial rank, a detailed CV or resume, concise statements on teaching and research, and full contact information for five professional references. Only complete applications will be considered. Review of applications will begin on January 4, 2016 and will continue until the position is filled.

1) Computer Architecture: <http://www.gwu.jobs/postings/29437>

2) Next Generation VLSI/CAD Design: <http://www.gwu.jobs/postings/29439>

Employment offers are contingent on the satisfactory outcome of a standard background screening.

The university is an Equal Employment Opportunity/Affirmative Action Employer that does not unlawfully discriminate in any of its programs or activities on the basis of race, color, religion, sex, national origin, age, disability, veteran status, sexual orientation, gender identity or expression, or on any other basis prohibited by applicable law.